

Amendment and Response

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Title: RANDOM ACCESS MEMORY USING PRECHARGE TIMERS IN TEST MODE

IN THE SPECIFICATION

Please amend the Summary as follows:

Summary

Embodiments of the present invention are illustrated in a random access memory. In one embodiment, the random access memory includes comprises memory banks and precharge timers configured to provide precharge signals to the memory banks. Each of the precharge timers corresponds to one of the memory banks and each of the precharge timers is configured to provide one of the precharge signals to the corresponding one of the memory banks in normal mode and in test mode.